

Amendments to the Specification:

Please insert the following new paragraph on page 1 immediately following the title:

This application claims the benefit of U.S. Provisional Application No. 60/427,422, filed November 18, 2002, and U.S. Provisional Application No. 60/499,197, filed August 28, 2003.

Please replace the paragraph beginning on page 7, line 26 with the following amended paragraph:

FIG. 7 illustrates an example block diagram of a compensated isolated level shifting circuit 70 for coupling signals between two isolated systems. The isolated systems include a first system with reference voltages Vdd1 and Vgnd1, and a second system with reference voltages Vdd2 and Vgnd2. The circuit 70 includes a voltage source Vos that is configured to offset the biasing of the level shifter 71. This offset voltage Vos biases the level shifter 71 sufficiently so that the voltage source Vdd1 consistently provides power to the level shifter 71, to avoid transient switching. A current generator Icomp provides a compensating current from the second system to the first system that offsets the current that is provided by the first voltage source to the second system, to provide a substantially zero net current flow between the systems. A diode D is connected between the current generator Icomp and the voltage source Vos. The offset voltage Vos could also be configured to bias the level shifter 71 so that Vdd2 provides the power to the level shifter 71, and so on. In like manner, the current generator Icomp could be configured to provide current from the first system to the second, or a combination of compensation generators could be used to substantially equalize the current flow from each system, and so on.

Please insert the following new paragraph immediately preceding the paragraph beginning on page 10, line 24:

FIG. 15 illustrates an example circuit diagram of a level shifting circuit for use in a level shifting system. The designations Vdd1 and Vgnd1 generally refer to a voltage reference of the input system. The designations Vdd2 and Vgnd2 generally refer to a voltage reference of the input system. The designation Vmax generally refers to the higher of Vdd1 and Vdd2. The designation Vout generally refers to an output voltage. The designation Iin and Vin generally refers to an input current and an input voltage, respectively. The designation Ibias generally refers to a bias current. The designations M1, M2, M3, M4, M5, M6, M7, M8, M9, M10, M11, M12, M13, M14, M15, M16, M17, M18, M21, M22, M30, M31, M32, M33 generally refer to transistors. At least some of the transistors M1, M2, M3, M4, M5, M6, M7, M8, M9, M10, M11, M12, M13, M14, M15, M16, M17, M18, M21, M22, M30, M31, M32, M33 are configured to form current mirrors.